

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

<p><i>gh</i> O.I.P.E.</p> <p>SCANNED <i>PD</i> Q.A. <i>!W</i></p>	PATENT DATE
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APPLICATION NO. 09/637532	CONT/PRIOR F	CLASS 708	SUBCLASS 210	ART UNIT 2787	EXAMINER Malzahn
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## APPLICANTS

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Peter Meulemans

**TITLE**

### Parallel counter and a multiplication logic circuit

PTO-2040  
12/99

<b>ISSUING CLASSIFICATION</b>																
<b>ORIGINAL</b>						<b>CROSS REFERENCE(S)</b>										
<b>CLASS</b>				<b>SUBCLASS</b>		<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>									
<b>INTERNATIONAL CLASSIFICATION</b>																

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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<b>ISSUE FEE</b>				
	_____ (Primary Examiner) _____ (Date)		Amount Due	Date Paid
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